

## IN THE SPECIFICATION

Please amend the specification as follows:

[0001] This application is related to copending and cofiled applications for United States Letters Patent Serial No. 09/918,023 \_\_\_\_\_, filed July 30, 2001 \_\_\_\_\_ and entitled METHOD AND APPARATUS FOR IN-SYSTEM PROGRAMMING THROUGH A COMMON CONNECTION POINT OF PROGRAMMABLE LOGIC DEVICES ON MULTIPLE CIRCUIT BOARDS OF A SYSTEM (~~Attorney Docket No. 10016249-1~~); Serial No. 09/918,030 \_\_\_\_\_, filed July 30, 2001 \_\_\_\_\_ and entitled FIRMWARE FOR ACCESSING SCAN CHAINS AND UPDATING EEPROM-RESIDENT FPGA CODE VIA A MANAGEMENT BUS TO JTAG BUS BRIDGE (~~Attorney Docket No. 10017840-1~~); Serial No. 09/718,024 \_\_\_\_\_ filed July 30, 2001 \_\_\_\_\_ and entitled METHOD AND APPARATUS FOR SERIAL BUS TO JTAG BUS BRIDGE (~~Attorney Docket No. 10017841-1~~); and Serial No. 09/917,982 \_\_\_\_\_, filed July 30, 2001 \_\_\_\_\_ and entitled METHOD FOR JUST-IN-TIME UPDATING OF PROGRAMMING PARTS (~~Attorney Docket No. 10017845-1~~), all of the aforementioned applications incorporated herewith by reference thereto.